




Pin No.	Symbol	Pin No.	Symbol	Pin No.	Symbol	Pin No.	Symbol
1	GND	1	GND	1	GND	1	GND
2	Host RX	2	A-	2	Host RX	2	UP
3	Host TX	3	B-	3	Host TX	3	DOWN
4	VCC	4	VCC	4	VCC	4	LEFT
						5	RIGHT
						6	ENTER

备注	此模块与X1F070-WSVGA-ST-01-V04共用PCB板
----	----------------------------------

1	2	3	4	5	6	7	8
---	---	---	---	---	---	---	---

		SCALE		FREE	TOLERANCE	±0.2
		UNIT	mm	DRG DATE	2025/07/19	
			DRAWN BY	TGG		
			DSN BY	TGG		
			CHECKED BY	LYQ		
			APPROVED BY	LYQ		
<div>深圳市尚视界科技有限公司</div> <div>Shenzhen Sampixels Technology Co.,LTD</div> <div>PROJECT NDXF070</div>						
		MIDDLE NDXF070-VVGA1-ST-01-V01				
		DCN	A4	EDITION		
		P 1 OF 1		A		
		<div></div>				