

<b>深圳上线科技有限公司</b>	Doc.No.:	
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# Specifications

## TFT-LCDmodule

**Model No: SX177QQVGA20P-02-V01**

<b>For Customer's Acceptance</b>	
<b>Approved by</b>	<b>Comment</b>

	<b>Signature</b>	<b>Date</b>
<b>Prepared by</b>		
<b>Checked by</b>		
<b>Approved by</b>		

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## 1 General Description

SX177QQVGA20P-02-V01 is a transmissive type a-Si TFT-LCD (amorphous silicon thin film transistor liquid crystal display) module, which is composed of a TFT-LCD panel, a driver circuit a backlight unit, The panel size is 1.77inch and thresolution is 128x160. High image quality a-Si TFT LCD module. Partial-screen display function is available. Sleep and Stand-by modes are available for power saving.

### 1.1 Features

No	Item	Specification	Remark
1	Display Mode	High Resolution & Wide View	
2	Screen Size	1.77inch (diagonal)	
3	Resolution	128XRGBX160	
4	Color Number	262K TFT	
5	Color Arrangement	RGB-stripe	
6	Driver IC	ST7735S	
7	Back Light	White LED*2	
8	Viewing Direction	12 O'clock	
9	Interface	MCU 8	
10	Surface Treatment	UV Cut	
11	Module brightness	300 cd/m <sup>2</sup>	

### 1.2 Application

- ◆ Mobile phone.
- ◆ Portable multimedia device.

## 2 Outline Dimension

The mechanical detail is shown in Fig. 1 and summarized in Table 1 below.

Parameter	Specifications	Unit
Outline dimensions	34.00(W) x43.7(H) x 2.5(D) (LCM,no include FPC)	mm
Active area	28.03(W) x35.04(H)	mm
Resolution	128(H)RGBx160(V) dots	-
Dot size	0.219x0.219	mm

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## 3 Module specification of the module

NOTES: Main LCD: TFT TRANSMISSIVE 12:00

1. DISPLAY TYPE: NORMAL WHITE
2. OPERATING TEMP: -20° C~70° C
3. STORAGE TEMP: -30° C~80° C
4. MAIN LCD DRIVER: ST7735S
5. BACKLIGHT: 2CHIP-WHITE LED
6. ROHS COMPLIANT
7. GENERAL TOLERANCE: ±0.2

Customer's Approval

Customer

Date

PIN	DESC
1	LEDK
2	LEDA
3	GND
4	VDD
5	VDD
6	D0
7	D1
8	D2
9	D3
10	D4
11	D5
12	D6
13	D7
14	GND
15	WR
16	CS
17	REST
18	RS
19	RD
20	GND

背光驱动: V<sub>F</sub>=2.9~3.2V, I<sub>F</sub>=20~20mA

REVISED RECORD				
A	B	C	D	E

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UNIT	SCALE	PAGE	DRAWN BY	QA/Q
mm	1:1	1/1	CHECKED BY	
PRODUCT NO: SK177001G42P-02-1/01			APPROVED BY	
			ORG DATE	

## 4 Electrical Characteristics

### 4.1 TFT-LCD Module

Parameter	Symbol	Condition	Specification			Unit	Related Pins
			MIN.	TYP.	MAX.		
Power & Operation Voltage							
System Voltage	VDD	Operating voltage	2.5	2.75	3.3	V	
Interface Operation Voltage	VDDI	I/O Supply Voltage	1.65	1.8	3.3	V	
Gate Driver High Voltage	VGH		10		15	V	
Gate Driver Low Voltage	VGL		-12.4		-7.5	V	
Gate Driver Supply Voltage		VGH-VGL	17.5		27.4	V	
Input / Output							
Logic-High Input Voltage	VIH		0.8VDDI		VDDI	V	Note 1
Logic-Low Input Voltage	VIL		VSS		0.2VDDI	V	Note 1
Logic-High Output Voltage	VOH	IOH = -1.0mA	0.8VDDI		VDDI	V	Note 1
Logic-Low Output Voltage	VOL	IOL = +1.0mA	VSS		0.2VDDI	V	Note 1
Logic-High Input Current	IIH	VIN = VDDI			1	uA	Note 1
Logic-Low Input Current	IIL	VIN = VSS	-1			uA	Note 1
Input Leakage Current	IIL	IOH = -1.0mA	-0.1		+0.1	uA	Note 1
VCOM Voltage							
VCOM amplitude	VCOM		-2		-0.425	V	
Source Driver							
Source Output Range	VSout		GVCL		GVDD	V	
Gamma Reference Voltage(Positive)	GVDD		3.15		4.7	V	
Gamma Reference Voltage(Negative)	GVCL		-4.7		-3.15	V	
Source Output Settling Time	Tr	Below with 99% precision			20	us	Note 2
Output Offset Voltage	VOFFSET				35	mV	Note 3

Table 2 Basic DC Characteristics

## 4.2 Back-Light Unit

Item	Symbol	Min.	Typ.	Max.	Unit	Remark
Current	IF	45	50	60	mA	IF=45mA IF=3.2V
Forward voltage	VF	2.9	3.2	3.5	V	
Chroma	X	0.250	--	0.30		
	Y	0.250	--	0.30		
Brightness	L	3000	--	--	Cd/m2	
Uniformity	UBL	80	--	--	%	

- 3 LEDs multiple circuit
- The luminous intensity of LED is strongly dependent on the driving current.
- It is recommended the input of backlight to be constant current rather than constant voltage.

## 5 TFT-LCM Interface Specification

端口号	代码	功能
1	LEDK	背光负极输入端
2	LEDA	背光正极输入端
3	GND	Ground (0V)
4	VDD	系统电压 (2.8V)
5	VDD	系统电压 (2.8V)
6-13	DB0-DB7	数据线
14	GND	Ground (0V)
15	WR	写使能信号
16	/CS	片选信号
17	/RESET	复位信号输入端
18	RS	指令/数据 选择端口：“H”：数据；“L”：指令
19	RD	读使能信号
20	GND	Ground (0V)

## 6 Description of Interface'Signal

### 6.1

VDDI=1.65 to 3.3V, VDD=2.5 to 3.3V, AGND=DGND=0V, T<sub>a</sub>=25 °C

Signal	Symbol	Parameter	MIN	MAX	Unit	Description
HSYNC, VSYNC	TSYNCS	VSYNC, HSYNC Setup Time	TBD		ns	
	Trghr, Trghf	VSYNC, HSYNC Rise/Fall time	TBD		ns	
ENABLE	TENS	Enable Setup Time	TBD		ns	
	TENH	Enable Hold Time	TBD		ns	
DOTCLK	PWDH	DOTCLK High-level Pulse Width	TBD		ns	
	PWDL	DOTCLK Low-level Pulse Width	TBD		ns	
	TCYCD	DOTCLK Cycle Time	TBD		ns	
	Trghr, Trghf	DOTCLK Rise/Fall time	TBD		ns	
DB	TPDS	PD Data Setup Time	TBD		ns	
	TPDH	PD Data Hold Time	TBD		ns	

Table 7 18/16 Bits RGB Interface Timing Characteristics

### 6.2

#### 7.2. DC Characteristics

Parameter	Symbol	Condition	Specification			Unit	Related Pins
			MIN.	TYP.	MAX.		
Power & Operation Voltage							
System Voltage	VDD	Operating voltage	2.5	2.75	3.3	V	
Interface Operation Voltage	VDDI	I/O Supply Voltage	1.65	1.8	3.3	V	
Gate Driver High Voltage	VGH		10		15	V	
Gate Driver Low Voltage	VGL		-12.4		-7.5	V	
Gate Driver Supply Voltage		VGH-VGL	17.5		27.4	V	
Input / Output							
Logic-High Input Voltage	VIH		0.8VDDI		VDDI	V	Note 1
Logic-Low Input Voltage	VIL		VSS		0.2VDDI	V	Note 1
Logic-High Output Voltage	VOH	IOH = -1.0mA	0.8VDDI		VDDI	V	Note 1
Logic-Low Output Voltage	VOL	IOL = +1.0mA	VSS		0.2VDDI	V	Note 1
Logic-High Input Current	I <sub>IH</sub>	VIN = VDDI			1	uA	Note 1
Logic-Low Input Current	I <sub>IL</sub>	VIN = VSS	-1			uA	Note 1
Input Leakage Current	I <sub>IL</sub>	IOH = -1.0mA	-0.1		+0.1	uA	Note 1
VCOM Voltage							
VCOM amplitude	VCOM		-2		-0.425	V	
Source Driver							
Source Output Range	VSout		GVCL		GVDD	V	
Gamma Reference Voltage(Positive)	GVDD		3.15		4.7	V	
Gamma Reference Voltage(Negative)	GVCL		-4.7		-3.15	V	
Source Output Settling Time	Tr	Below with 99% precision			20	us	Note 2
Output Offset Voltage	VOFFSET				35	mV	Note 3

Table 2 Basic DC Characteristics



## 6.3 AC Characteristics



VDDI=1.65 to 3.3V, VDD=2.5 to 3.3V, AGND=DGND=0V, Ta=25 °C

Signal	Symbol	Parameter	Min	Max	Unit	Description
DCX	TAST	Address Setup Time	TBD		ns	
	TAHT	Address Hold Time (Write/Read)	TBD		ns	
WRX	TWC	Write Cycle	TBD		ns	
	TWRH	Control Pulse "H" Duration	TBD		ns	
	TWRL	Control Pulse "L" Duration	TBD		ns	
RDX	TRC	Read Cycle (ID)	TBD		ns	When Read ID Data
	TRDH	Control Pulse "H" Duration (ID)	TBD		ns	
	TRDL	Control Pulse "L" Duration (ID)	TBD		ns	
DB[17:0]	TDST	Data Setup Time	TBD		ns	TRAT, TRATFM: 3K ohm Pull up or Down
	TDHT	Data Hold Time	TBD		ns	

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Signal	Symbol	Parameter	Min	Max	Unit	Description
	TRAT	Read Access Time (ID)		TBD	ns	and 30pF Parallel Cap. To GND.
	TODH	Output Disable Time	TBD	TBD	ns	TODH: 3K ohm Pull up or Down.

Table 4 8080 Parallel Interface Characteristics

## 6.4

### 10.4.. Reset Timing Characteristic

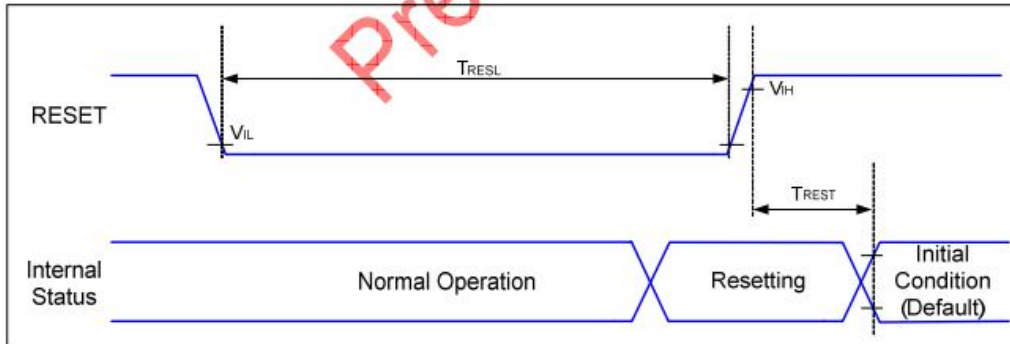


Figure 51 Reset Timing

VDDI=1.65 to 3.3V, VDD=2.5 to 3.3V, AGND=DGND=0V, Ta=25 °C

Signal	Symbol	Parameter	Min	Max	Unit	Description
RESET	TRESL	Reset Low Level Width	1	-	ms	-
	TREST	Reset Complete Time	1		ms	

Table 11 Reset timing Characteristics

## 7. Optical Specification

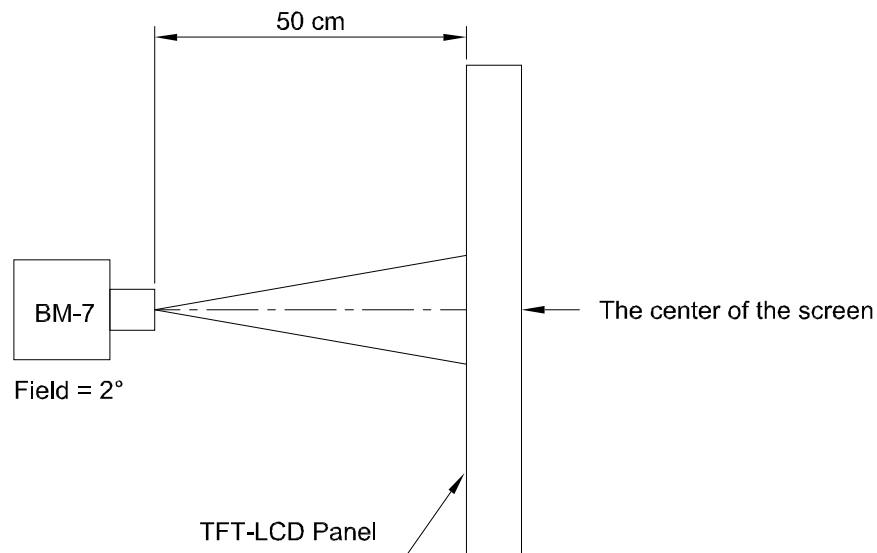
The optical characteristics are measured under stable conditions as following notes.

**Table 3 Optical Characteristics**

Item	Conditions	Min.	Typ.	Max.	Unit	Note
Viewing Angle (CR>10)	Horizontal	$\theta_{x+}$	-	(45)	-	deg. (1),(2),(6),(7), (8)
		$\theta_{x-}$	-	(45)	-	
	Vertical	$\theta_{y+}$	-	(45)	-	
		$\theta_{y-}$	-	(20)	-	
Contrast Ratio	Center	-	(500)	-	-	(1),(3),(6)(7), (8)
Response Time	Rising + Falling	-	(20)	-	ms	(1),(4),(6),(7), (8)
CF Color Chromaticity (CIE1931)	Red x	Typ. -0.03	(0.626)	Typ. +0.03	-	Under C-light
	Red y		(0.334)		-	
	Green x		(0.277)		-	
	Green y		(0.549)		-	
	Blue x		(0.142)		-	
	Blue y		(0.122)		-	
	White x		(0.303)		-	
	White y		(0.325)		-	
NTSC	CIE1931	-	(60)	-	%	(1),(6),(7),(8)
Transmittance	Without POL	-	(15.7)	-	%	(1),(5),(6),(7), (8)

Note 1: The brightness test equipment setup

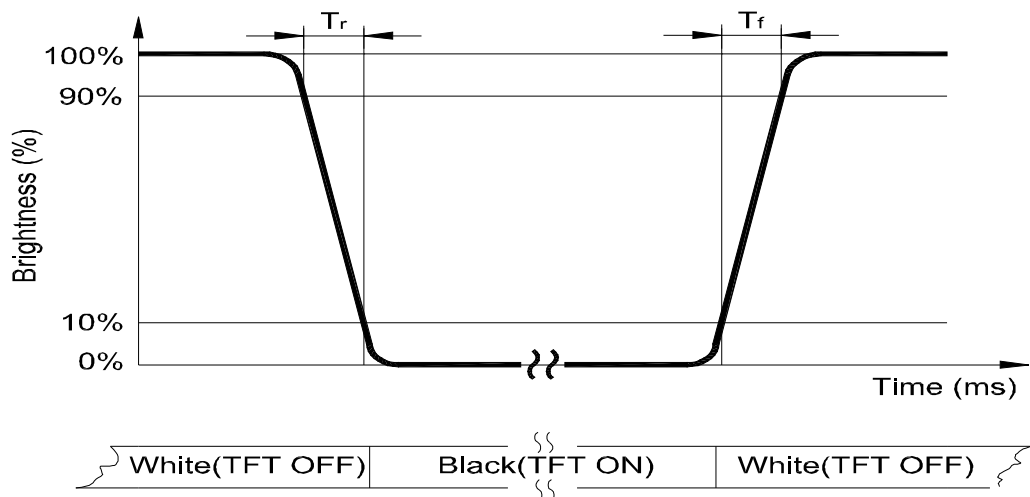
$I_B=60\text{mA}$ , Field= $2^\circ$  (As measuring "black" image, field= $2^\circ$  is the best testing condition.)



Note 2: Definition of contrast ratio (C.R)

$$C.R = \frac{\text{Brightness When LCD is at "White" State}}{\text{Brightness When LCD is at "Black" State}}$$

Note 3: Definition of response time



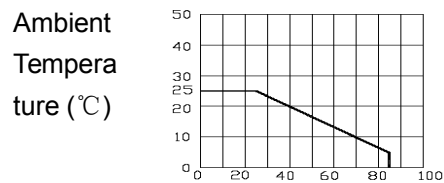


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## 8 Environment Absolute Maximum Ratings

Item	Symbol	Min	Max	Unit	Remark
Operation temperature range	Top	-20	70	°C	Ambient
Storage temperature range	Tst	-30	80	°C	Ambient

- Corrosive gas environment is not acceptable.
- TFT-LCD color will change slightly depending on environment temperature. This phenomenon is reversible. Current reduction rate of LED backlight is according to the graph indicated below:



Allowable Forward Current (mA)

## 9 Reliability Test Items

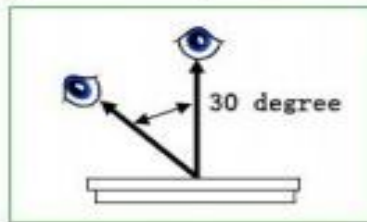
Item	Test Condition		Criterion
High Temperature Storage	80 °C, 240 hrs		There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.
Low Temperature Storage	-30 °C, 240 hrs		
High Temp. & High Humidity Storage	60 °C, 90% RH, 240 hrs		
Vibration Test (Non-operating)	Freq.:10~55~10 Hz, Amp.:1.5mm 1 hr for each direction of X, Y, Z		
Electrostatic Discharge Test (Non-operating)	Terminals	150 pF, 0 Ω, ±300 V, Contact	
	Panel	150 pF, 330 Ω, ±8 KV, Air	
Thermal Shock (Static)	-30°C, 30 min /80°C, 30 min, 20 cycles		
High Temperature Operation	70 °C, 240 hrs		
Low temperature Operation	-20 °C, 240 hrs		
High Temperature & High Humidity (Operating)	50 °C, 90% RH, 240 hrs		
FPC Peeling Strength Test	Pull speed: 50 mm/min, +90°,		> 400gf/cm

## 10 Inspection Standard

This standard apply to TFT module specification.

### 1. Inspection condition:

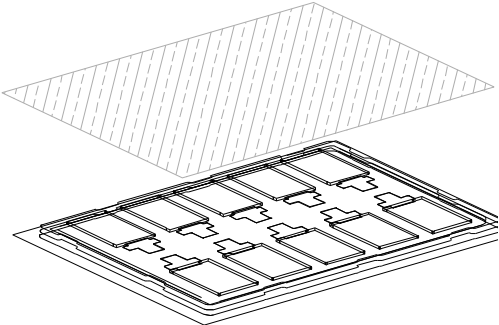
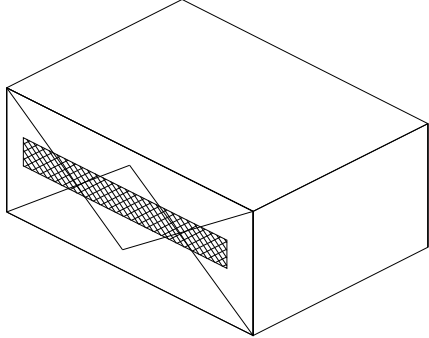
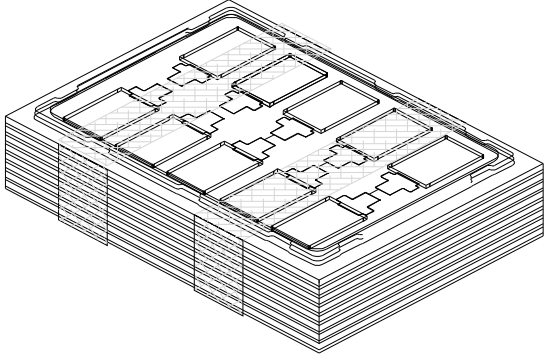
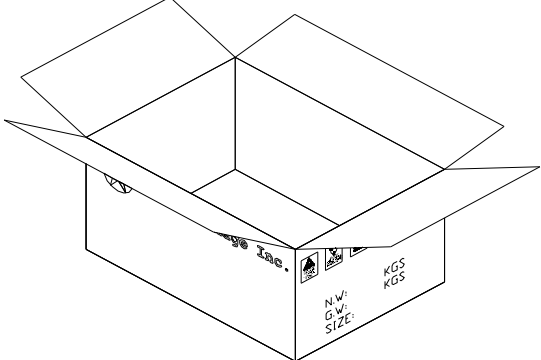
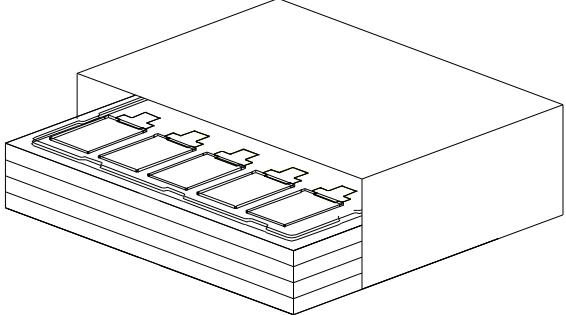
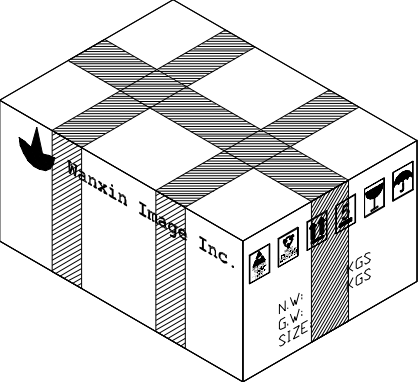
Under daylight lamp 20~40W, product distance inspector'eye 30cm.incline degree 30° .



### 2. Inspection standard

NO.	Item	Inspection standard	Rate															
2.1	Dot	<p>Case of Dot defect is below</p> <p>① Bright Dot (whit spot) : "0"</p> <p>② Dark Dot (black spot) : "0" (In case of Dark Dot on Main TFT LCD)</p> <p>- NG if there's full Dot defect.</p> <p>- Damaged less than the size of sub-pixel is not counted as defect</p> <p>- Dots darker than the size of sub-pixel are not defined as bright dot defect</p> <table border="1"> <thead> <tr> <th>area size (mm)</th> <th>Acceptable number</th> </tr> </thead> <tbody> <tr> <td><math>\Phi \leq 0.10</math></td> <td>ignore</td> </tr> <tr> <td><math>0.10 &lt; \Phi \leq 0.15</math></td> <td>3</td> </tr> <tr> <td><math>0.15 &lt; \Phi \leq 0.20</math></td> <td>2</td> </tr> <tr> <td><math>0.25 &lt; \Phi \leq 0.25</math></td> <td>1</td> </tr> <tr> <td><math>0.25 &lt; \Phi</math></td> <td>0</td> </tr> </tbody> </table>	area size (mm)	Acceptable number	$\Phi \leq 0.10$	ignore	$0.10 < \Phi \leq 0.15$	3	$0.15 < \Phi \leq 0.20$	2	$0.25 < \Phi \leq 0.25$	1	$0.25 < \Phi$	0	minor			
area size (mm)	Acceptable number																	
$\Phi \leq 0.10$	ignore																	
$0.10 < \Phi \leq 0.15$	3																	
$0.15 < \Phi \leq 0.20$	2																	
$0.25 < \Phi \leq 0.25$	1																	
$0.25 < \Phi$	0																	
2.2	line	<table border="1"> <thead> <tr> <th colspan="2">Size (mm)</th> <th>Acceptable number</th> </tr> </thead> <tbody> <tr> <td>ignore</td> <td><math>W \leq 0.03</math></td> <td>ignore</td> </tr> <tr> <td><math>L \leq 4.0</math></td> <td><math>0.03 &lt; W \leq 0.04</math></td> <td>2</td> </tr> <tr> <td><math>L \leq 4.0</math></td> <td><math>0.04 &lt; W \leq 0.05</math></td> <td>1</td> </tr> <tr> <td></td> <td><math>0.05 &lt; W</math></td> <td>Treat with dot non-conformance</td> </tr> </tbody> </table>	Size (mm)		Acceptable number	ignore	$W \leq 0.03$	ignore	$L \leq 4.0$	$0.03 < W \leq 0.04$	2	$L \leq 4.0$	$0.04 < W \leq 0.05$	1		$0.05 < W$	Treat with dot non-conformance	
Size (mm)		Acceptable number																
ignore	$W \leq 0.03$	ignore																
$L \leq 4.0$	$0.03 < W \leq 0.04$	2																
$L \leq 4.0$	$0.04 < W \leq 0.05$	1																
	$0.05 < W$	Treat with dot non-conformance																

## 11 Package

<p>1</p> 	<p>4</p> 
<p>10 pcs per tray + 1 cover (EPE)</p>	<p>Packing bag</p>
<p>2</p> 	<p>5</p> 
<p>15 trays + 1 dummy tray = 150 ps</p>	<p>Putting bag into carton Protected by 2 pieces of cushion EPE sheet</p>
<p>3</p> 	<p>6</p> 
<p>Putting trays into anti-electrostatic bag</p>	<p>Packing carton with sealing tape Carton outline size: 400×295×145 (mm)</p>



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## 12 Precautions

Please pay attentions to the followings as using the LCD module.

### 12.1 Handling

- (a) Do not apply strong mechanical stress like drop, shock or any force to LCD module. It may cause improper operation, even damage.
- (b) Because the polarizer is very fragile and easy to be damaged, do not hit, press or rub the display surface with hard materials.
- (c) Do not put heavy or hard material on the display surface, and do not stack LCD modules.
- (d) If the display surface is dirty, please wipe the surface softly with cotton swab or clean cloth.
- (e) Avoid using Ketone type materials (e.g. Acetone), Toluene, Ethyl acid or Methyl chloride to clean the display surface. It might damage the touch panel surface permanently. The recommended solvents are water and Isopropyl alcohol.
- (f) Wipe off water droplets or oil immediately.
- (g) Protect the LCD module from ESD. It will damage the LSI and the electronic circuit.
- (h) Do not touch the output pins directly with bare hands.
- (i) Do not disassemble the LCD module.
- (j) Do not lift the FPC of Touch Panel.

### 12.2 Storage

- (a) Do not leave the LCD modules in high temperature, especially in high humidity for a long time.
- (b) Do not expose the LCD modules to sunlight directly.
- (c) The liquid crystal is deteriorated by ultraviolet. Do not leave it in strong ultraviolet ray for a long time.
- (d) Avoid condensation of water. It may cause improper operation.

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- (e) Please stack only up to the number stated on carton box for storage and transportation. Excessive weight will cause deformation and damage of carton box.

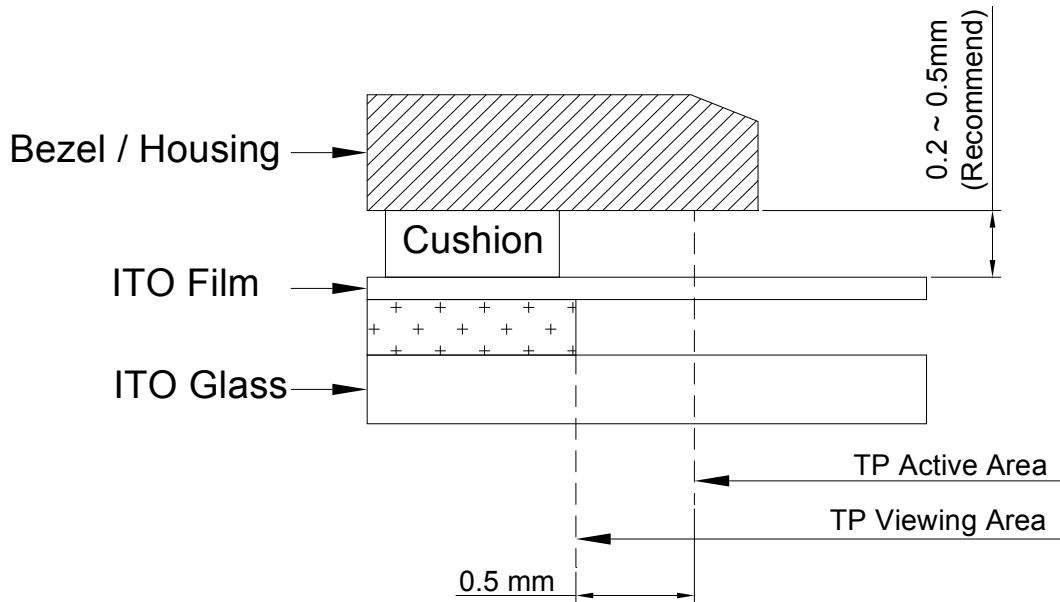
### **12.3 Operation**

- (a) When mounting or dismantling the LCD modules, turn the power off.
- (b) Protect the LCD modules from electric shock.
- (c) The Driver IC control algorithms stated above should always obeyed to avoid damaging the LSI and electronic circuit.
- (d) Be careful to avoid mixing up the polarity of power supply for backlight.
- (e) Absolute maximum rating specified above has to be always kept in any case. Exceeding it may cause non-recoverable damage of electronic components or, nevertheless, burning.
- (f) When a static image is displayed for a long time, remnant image is likely to occur.
- (g) Be sure to avoid bending the FPC to an acute shape, it might break FPC.
- (h) Most of the touch screens have air vent to equalize the inside air pressure to the outside one. The air vent must be open and liquid contact must be avoided as the liquid may be absorbed if the liquid is accumulated near the air vent.
- (i) For the fragility of ITO film, it should avoid to use too tapering pen as the input material.

### **12.4 Touch Panel Mounting Notes**

- (a) If a cushion is used between bezel/housing and film must be choose as free as enough to absorb the expansion and contraction to avoid the distortion of film.
- (b) The cushion must be placed out of the Viewing Area.
- (c) Bezel/Housing edge must be posited between Key Area and Viewing Area. The edge enters the Key Area may cause unexpected input if the gap is too narrow or foreign particles like dusts exist between Bezel/Housing and ITO film.
- (d) Mounting example:

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The corner part has conductivity. Do not touch any metal part after mounting.

### 12.5 Others

- a) If the liquid crystal leaks from the panel, it should be kept away from the eyes or mouth.
- b) For the fragility of polarizer, it is recommended to attach a transparent protective plate over the display surface.
- c) It is recommended to peel off the protection film on the polarizer slowly so that the electrostatic charge can be minimized.

## 13 Records of Version

Version	Revise Date	Page	Content
A	2023-03-09	All	New released