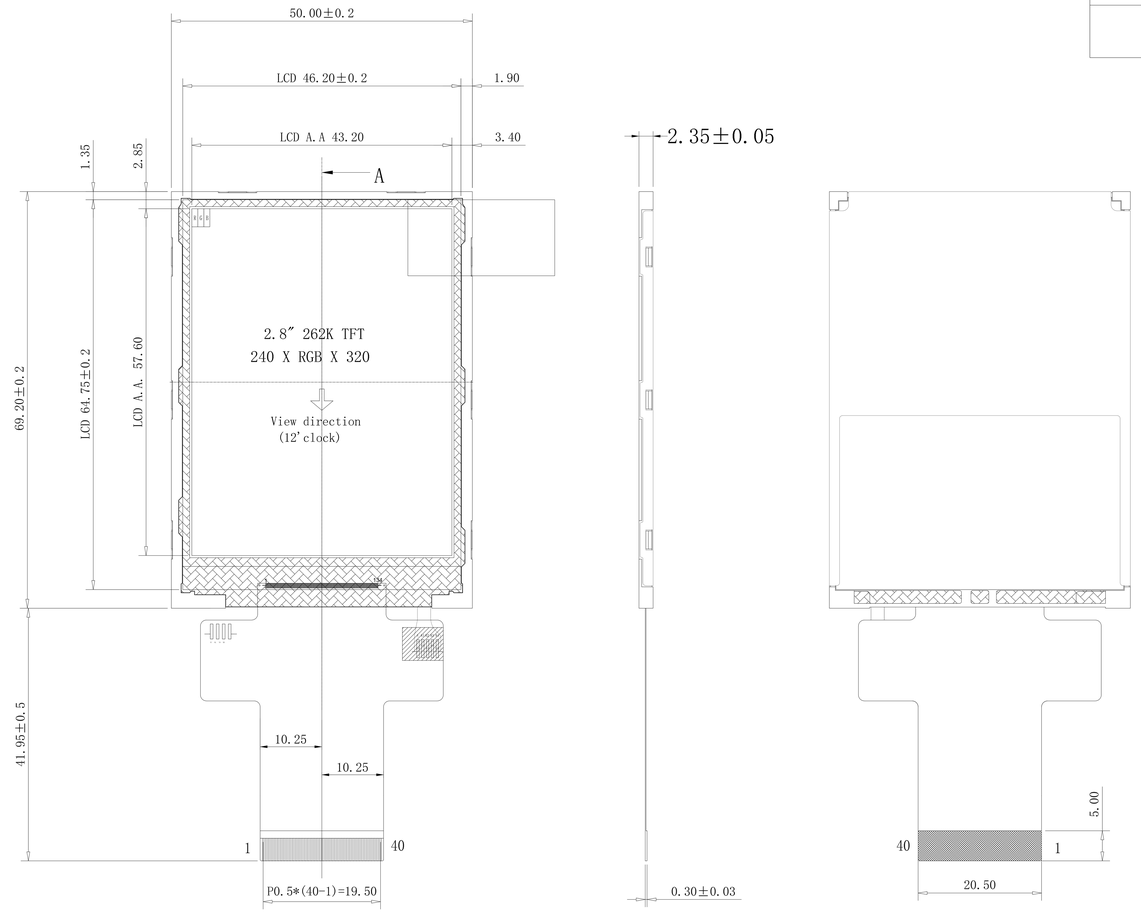
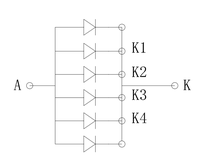
REV	ECN NO.	NATURE OF MODIFICATION	CHANGED BY	DATE



1	X(L)
2	Y(U)
3	X(R)
4	Y(D)
5	GND
6	IOVCC(1.8/2.8V)
7	VCI(2.8V)
8	FMARK
9	CS/SPI CS
10	RS/(SPI SCL/SCK)
11	WR/(SPI_RS 4WIRE)
12	RD
13	SPI SDI/SDA
14	SPI SDO
15	RESET
16	GND
17	DB0
18	DB1
19	DB2
20	DB3
21	DB4
22	DB5
23	DB6
24	DB7
25	DB8
26	DB9
27	DB10
28	DB11
29	DB12
30	DB13
31	DB14
32	DB15
33	A
34	K
35	K
36	K
37	GND
38	IM0
39	IM1
40	IM2

IM2	IM1	IM0	MUC 模式	USED BUS
0	0	0	80 MUC 16bit	DB(0-15)
0	0	1	80 MUC 8bit	DB(8-15)
1	0	1	3WIRE 9bit	SDI:IN; SDO:OUT
1	1	0	4WIRE 8bit	SDI:IN; SDO:OUT

ITEM	ELECTRICAL & PHYSICAL DATA
LCD Type	2.8' TFT
Viewing Direction	12 O'CLOCK
Drive Method	1/320 DUTY
VDD	2.8V
Display Mode	Transmissive
	-20° C ~ 70° C
Storage Temp.	-30° C ~ 80° C
Connector(IC)	ST7789V



Back Light Circuit

DESIGNED BY:	CHECKED BY:	APPROVED BY:	UNIT:mm	3RD ANGLE	SCALE; 比例

深圳市上线电子有限公司

MODEL NAME: SX028QVGA40P001 V01

DWG NO:

REV:A

SH NO:1/1